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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/830,097	04/23/2004	Yoshiaki Okuno	0925-0179PUS2	7525
2292 7590 06/12/2007 BIRCH STEWART KOLASCH & BIRCH PO BOX 747			EXAMINER .	
			LESPERANCE, JEAN E	
FALLS CHURCH, VA 22040-0747			ART UNIT	PAPER NUMBER
			2629	
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			NOTIFICATION DATE	DELIVERY MODE
			06/12/2007	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

mailroom@bskb.com

	Amplication No.	Anntinemate)		
	Application No.	Applicant(s)		
Office Action Summan	10/830,097	OKUNO ET AL.		
Office Action Summary	Examiner	Art Unit		
	Jean E. Lesperance	2629		
The MAILING DATE of this communication Period for Reply	n appears on the cover sheet wit	h the correspondence address		
A SHORTENED STATUTORY PERIOD FOR RI WHICHEVER IS LONGER, FROM THE MAILIN - Extensions of time may be available under the provisions of 37 CI after SIX (6) MONTHS from the mailing date of this communicatio - If NO period for reply is specified above, the maximum statutory p - Failure to reply within the set or extended period for reply will, by s Any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	IG DATE OF THIS COMMUNIC FR 1.136(a). In no event, however, may a re on. beriod will apply and will expire SIX (6) MONT statute, cause the application to become ABA	CATION. Poply be timely filed FHS from the mailing date of this communication. ANDONED (35 U.S.C. § 133).		
Status				
1) Responsive to communication(s) filed on	23 April 2004.			
2a) ☐ This action is FINAL . 2b) ☑	This action is FINAL . 2b)⊠ This action is non-final.			
3) Since this application is in condition for all	•	•		
closed in accordance with the practice und	der <i>Ex par</i> te Quayle, 1935 C.D.	11, 453 O.G. 213.		
Disposition of Claims				
4) Claim(s) <u>1-6</u> is/are pending in the applicat	ion.			
4a) Of the above claim(s) is/are with				
5)⊠ Claim(s) <u>1-4</u> is/are allowed.				
6)⊠ Claim(s) <u>5 and 6</u> is/are rejected.				
7) Claim(s) is/are objected to.				
8) Claim(s) are subject to restriction a	nd/or election requirement.			
Application Papers				
9) The specification is objected to by the Exa	miner.			
10)⊠ The drawing(s) filed on 23 April 2004 is/are	e: a)⊠ accepted or b)⊡ objec	ted to by the Examiner.		
Applicant may not request that any objection to	the drawing(s) be held in abeyand	ce. See 37 CFR 1.85(a).		
Replacement drawing sheet(s) including the co	orrection is required if the drawing(s) is objected to. See 37 CFR 1.121(d).		
11)☐ The oath or declaration is objected to by th	ne Examiner. Note the attached	Office Action or form PTO-152.		
Priority under 35 U.S.C. § 119				
12)⊠ Acknowledgment is made of a claim for for a)⊠ All b)□ Some * c)□ None of: 1.⊠ Certified copies of the priority docur		119(a)-(d) or (f).		
2. Certified copies of the priority docur	ments have been received in Ap	oplication No		
3. Copies of the certified copies of the	priority documents have been i	received in this National Stage		
application from the International Bu	, , , , , , , , , , , , , , , , , , , ,			
* See the attached detailed Office action for a	a list of the certified copies not r	eceived.		
Attachment(s)				
Notice of References Cited (PTO-892)		ummary (PTO-413)		
 2))/Mail Date formal Patent Application		
Paper No(s)/Mail Date <u>4/23/04</u> .	6) Other:			

DETAILED ACTION

1. The application filed April 23, 2004 is presented for examination and claims 1-6 are pending.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 5 and 6 are rejected under 35 U.S.C. 102(b) as being unpatentable over USPN 5,781,185 by Shin.

Regarding claim 5, Shin teaches an output synchronization signal generation module configured to generate output vertical and horizontal synchronization signals based on an input horizontal synchronization signal and an input vertical synchronization signal (a first and a second synchronization detecting units 10 and 20 which respectively receive a vertical synchronization signal VSYNC and a horizontal synchronization signal HSYNC and both receive a clock signal MCLK. Detecting units 10 and 20 eliminate high-end noise of each signal and output VSYNC-O and HSYNC-O signals, respectively (column 2, lines 56-61)) (see Figure 5); and

an image conversion module configured to generate an output image signal based on an input image signal and the output vertical and horizontal synchronization

signals (an image is generation as seen in Figs.4A-4C based on the input signal from the clock signal Fig.5 (MCLK) and the output vertical and horizontal synchronization signals VSYN and HSYN from the MUX 60 and 60 (see Figure 5)) which is interpreted as the image conversion module.

Regarding claim 6, Shin teaches generating output vertical and horizontal synchronization signals based on an input horizontal synchronization signal and an input vertical synchronization signal (a first and a second synchronization detecting units 10 and 20 which respectively receive a vertical synchronization signal VSYNC and a horizontal synchronization signal HSYNC and both receive a clock signal MCLK.

Detecting units 10 and 20 eliminate high-end noise of each signal and output VSYNC-O and HSYNC-O signals, respectively (column 2, lines 56-61)) (see Figure 5); and

generating an output image signal based on an input image signal and the output vertical and horizontal synchronization signals (an image is generation as seen in Figs.4A-4C based on the input signal from the clock signal Fig.5 (MCLK) and the output vertical and horizontal synchronization signals VSYN and HSYN from the MUX 60 and 60 (see Figure 5)).

Allowable Subject Matter

- 3. Claims 1-4 are allowed.
- 4. The following is an examiner's statement of reasons for allowance: the claimed invention is directed to an image display device.

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Independent claim 1 identifies a uniquely distinct feature "a second calculation circuit obtaining a cycle information of the output horizontal synchronizing signal, based on the ideal cycle of the horizontal synchronizing signal and the panel clock signal and a horizontal synchronizing signal generator for generating the output horizontal synchronizing signal, based on the cycle information".

Independent claim 2 identifies a uniquely distinct feature "obtaining a cycle information of the output horizontal synchronizing signal, based on the ideal cycle of the output horizontal synchronizing signal and a panel clock signal used to display the converted image and generating the output horizontal synchronizing signal, based on the cycle information".

Independent claim 3 identifies a uniquely distinct feature "a controller for generating a control signal for controlling a cycle of an output horizontal synchronizing signal used for reading out the accumulated image data from the memory, according to image size information of the input image signals, cycle information of the input horizontal synchronizing signals, and cycle information of the second clock and a synchronizing signal generator for outputting the output horizontal synchronizing signal based on the control signal".

Independent claim 4 identifies a uniquely distinct feature "generating a control signal for controlling a cycle of an output horizontal synchronizing signal used for reading out the accumulated image data, according to image size information of the input image signals, cycle information of the input horizontal synchronizing signals, and

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cycle information of the second clock and outputting the output horizontal synchronizing signal based on the control signal".

Conclusion

5. Any inquiry concerning this communication or earlier communications from the ably examiner should be directed to Jean Lesperance whose telephone number is (571) 272-7692. The examiner can normally be reached on from Monday to Friday between 10:OOAM and 6:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Hjerpe, can be reached on (571) 272-7691.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to:

(571) 273-8300 (for Technology Center 2600 only)

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

Jean Lesperance

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Date 6/1/2007

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